

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Victor Suen et al.

Group. No.

Not yet assigned

Application No. 10/601,441

Examiner

Not yet assigned

Filed:

June 23, 2003

Title:

SYSTEMS AND METHODS FOR LATCHING DATA

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT **FILED UNDER 37 C.F.R. § 1.97(b)**

Pursuant to 37 C.F.R. § 1.97(b), Applicants hereby submit an Information Disclosure Statement by Applicant, Substitute form 1449/A, together with copies of all patents, publications or other information of which Applicants are aware, which Applicants believe may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

The relevance of the attached references is that this is the closest art of which Applicants are aware. Applicants submit that the above references taken alone or in combination neither anticipate nor render obvious the present invention. Consideration of the foregoing in relation to this application is respectfully requested.

It is requested that the information disclosed herein be made of record in this application.

Should any fees be required in connection with the filing of this Information Disclosure Statement, the Commission is hereby authorized to charge said fees to Deposit Account No. 12-2252.

Date: August 6, 2003

By:

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Respectfully submitted

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PTO/SB/08a (05-03) Approved for use through 04/30/2003. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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U.S. PATENT DOCUMENTS						
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of 2

Complete if Known

Application Number 10/601,441

Filing Date June 23, 2003

First Named Inventor Victor Suen

Group Art Unit

Examiner Name

Attorney Docket Number 02-6050

(use as many sheets as necessary)

Examiner Initials * No. Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), published, roity and/or country where published.

DDR SDRAM Functionality and Controller Read Data Capture, Micron Techonology, Inc., 1999, Volume 8, Issue 3 3099, pp. 1-24, Boise, Idaho

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1	Examiner	Date	
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